PTO/SB/31 (09-04)
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(37 CFR 1.191)

Docket Number (Optional)

SYN-0551CON1

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1.8(a)] 3/16/2006 Signature / Remode A Brunnan	For "Design Verification By Symbolic Simulation Using A Native Hardware Description Language"	
Typed or printed name Rebecca A. Baumann	Art Unit 2825	Examiner Stacy Whitmore
Applicant hereby appeals to the Board of Patent Appeals and Interferences from the last decision of the examiner.		
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 Applicant claims small entity status. See 37 CFR 1.27. Therefore, the fee shown above is reduced by half, and the resulting fee is: 		
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Jeanette S. Harms, Esq.

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This collection of information is required by 37 CFR 41.31. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11, 1.14 and 41.6. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED

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See 37 CFR 3.71. Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)

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